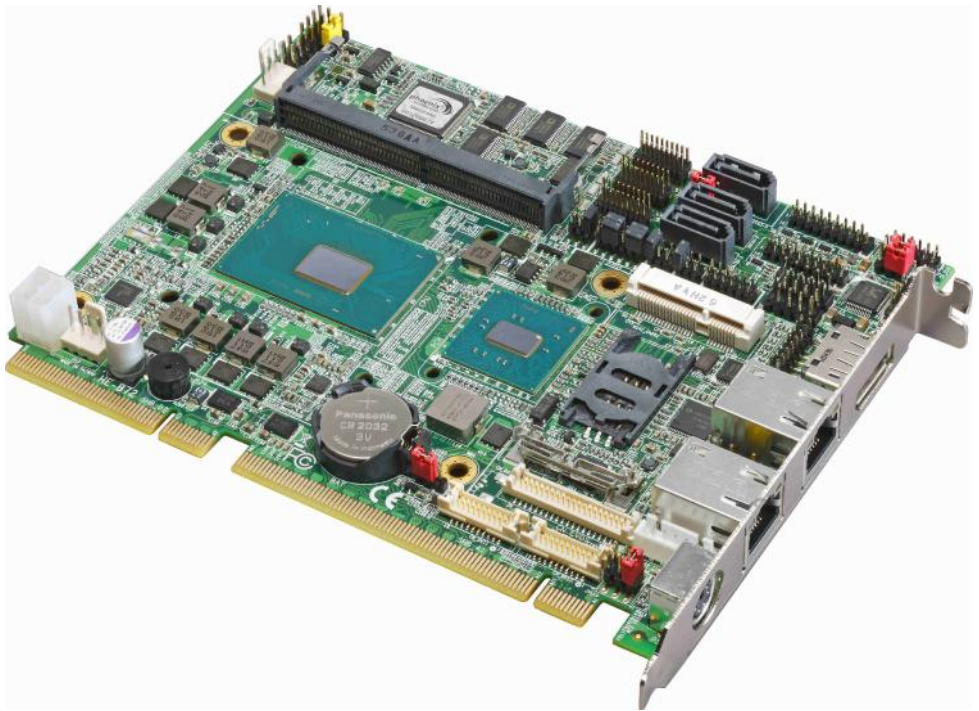


HE-B72

PICMG 1.3 Half-size CPU Card

User's Manual

Edition 1.6
2017/10/24



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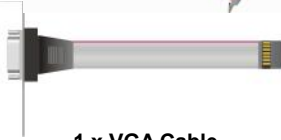
Any questions please visit our website at <http://www.commell.com.tw>

Packing List:

Please check the package content before you starting using the board.



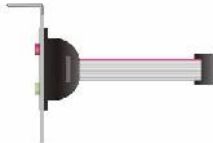
1 x HE-B72 PICMG 1.3 Half-size CPU Card
(include Cooler Fan)



1 x VGA Cable
(OALVGA-S-7) / (1040556)



2 x SATA Cable
(OALSATA3-L) / (1040529)



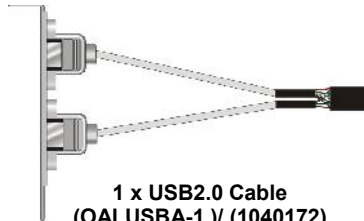
1 x Audio Cable
(OALPJ-HD) / (1040120)



1 x PS/2 Keyboard & Mouse Cable
(OALPS2/MKN) / (1040551)



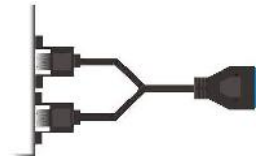
1 x Dual COM PORT Cable
(OALES-BKU2) / (1040087)



1 x USB2.0 Cable
(OALUSBA-1) / (1040172)



1 x DVI module with bracket
(BADPDVIP_A & OALDVI-DF13)
(4120008021 & 1040483)



1 x USB3.0 Cable
(OALUSB3) / (1040531)
(Optional)

Printed Matters:

Driver CD (Including User's Manual) x 1

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Chapter 1 <Introduction>

1.1 <Product Overview>

HE-B72 is PICMG 1.3 Half-size CPU Card which supports 6th Generation Intel® Core™ i7, i5, i3, H-series Processor with Intel® QM170 Chipset, integrated HD Graphics 530, DDR4 memory, Realtek High Definition Audio, Intel Gigabit LAN, Serial ATA3

Intel Skylake-H Processor with Intel® QM170 Chipset PCH-H

The 6th Generation Intel® Core™ H-series processor family is new generation and multi-core processor built on 14 nanometer process.

Skylake provide new HD Graphics 530 support triple displays at the same time, maximum supported is up to 32GB of DDR4, better performance, flexibility and more enhanced security that is suitable for a variety of intelligent systems the ideal choice.

All in One multimedia solution

HE-B72 provides high performance onboard graphics, 18/24-bit single/dual channel LVDS interface, DisplayPort, DVI-D, VGA and High Definition Audio, to meet the requirement of the multimedia application.

Flexible Expansion Interface

It includes two minicard slot, 6 x COM port, 4 x USB3.0, and 4 x USB2.0. Support PCIeX16 slot, PCIeX4 or PCIeX1 slot, 1 x PCI slot. (Backplane)

Skylake remove EHCI, all USB ports are xHCI

When you install Windows7 with USB device(CDROM, Keyboard, Mouse...), Windows7 can not identify your usb device. You can use SATA CD-ROM and PS/2 to install Windows7.

1.2 <Product Specification>

System

Processor	Intel® Skylake Core™ i7, i5, i3 H-series Processor, FCBGA1440 package
Chipset	Intel® QM170 PCH-H
Memory	2 x DDR4 SO-DIMM 2133 MHz up to 32GB, Support Non-ECC, unbuffered memory only
Watchdog Timer	Generates a system reset with internal timer for 1min/s ~ 255min/s
Real Time Clock	Chipset integrated RTC with onboard lithium battery
Expansion	1 x MiniPCle (support mSATA) 1 x Half-Size MiniPCle 1 x PCIe X16 slot, 1 x PCIe X4 or 4 x PCIe X1 slot (optional) 1 x Sim slot

Graphics

Chipset	Intel® 9th Gen integrated HD Graphics
Display Interface	2 x DVI-D, 1 x DisplayPort, 1 x LVDS, 1 x VGA

LAN

Chip	1 x Intel® I219-LM Gigabit PHY LAN (Support iAMT11.0) 1 x Intel® I210-AT Gigabit LAN
------	-----------------------------------------------------------------------------------------

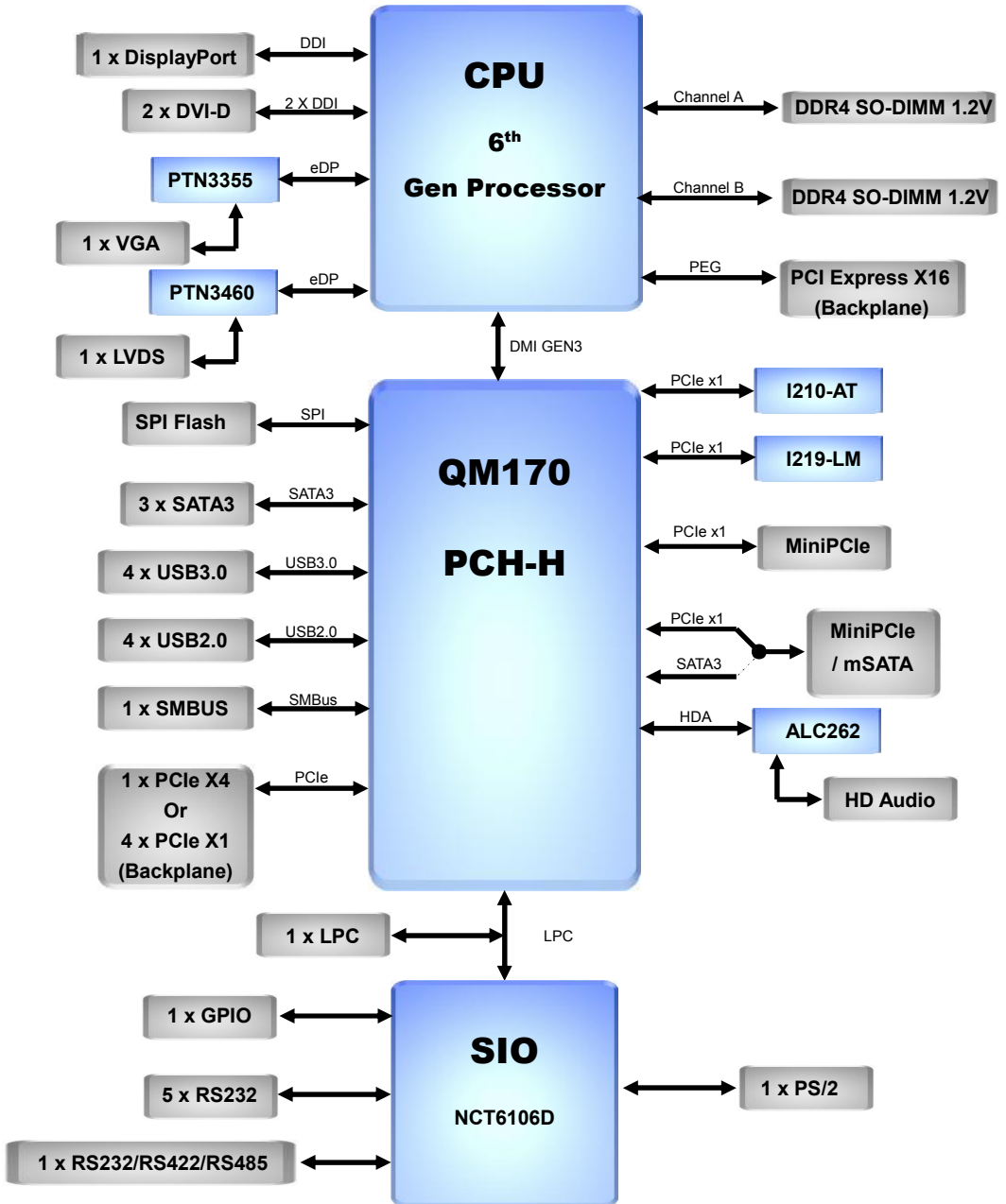
I/O

Serial ATA	3 x SATA3
Audio	Realtek ALC262 HD Audio
Internal I/O	3 x SATA3, 5 x RS232, 4 x USB2.0, 4 x USB3.0, 1 x LVDS, 1 x LPC 1 x LCD inverter, 1 x GPIO, 1 x Audio, 1 x SMBUS, 1 x VGA, 2 x DVI-D 1 x RS232/422/485
Rear I/O	1 x DisplayPort, 2 x LAN, 1 x PS/2

Mechanical & Environmental

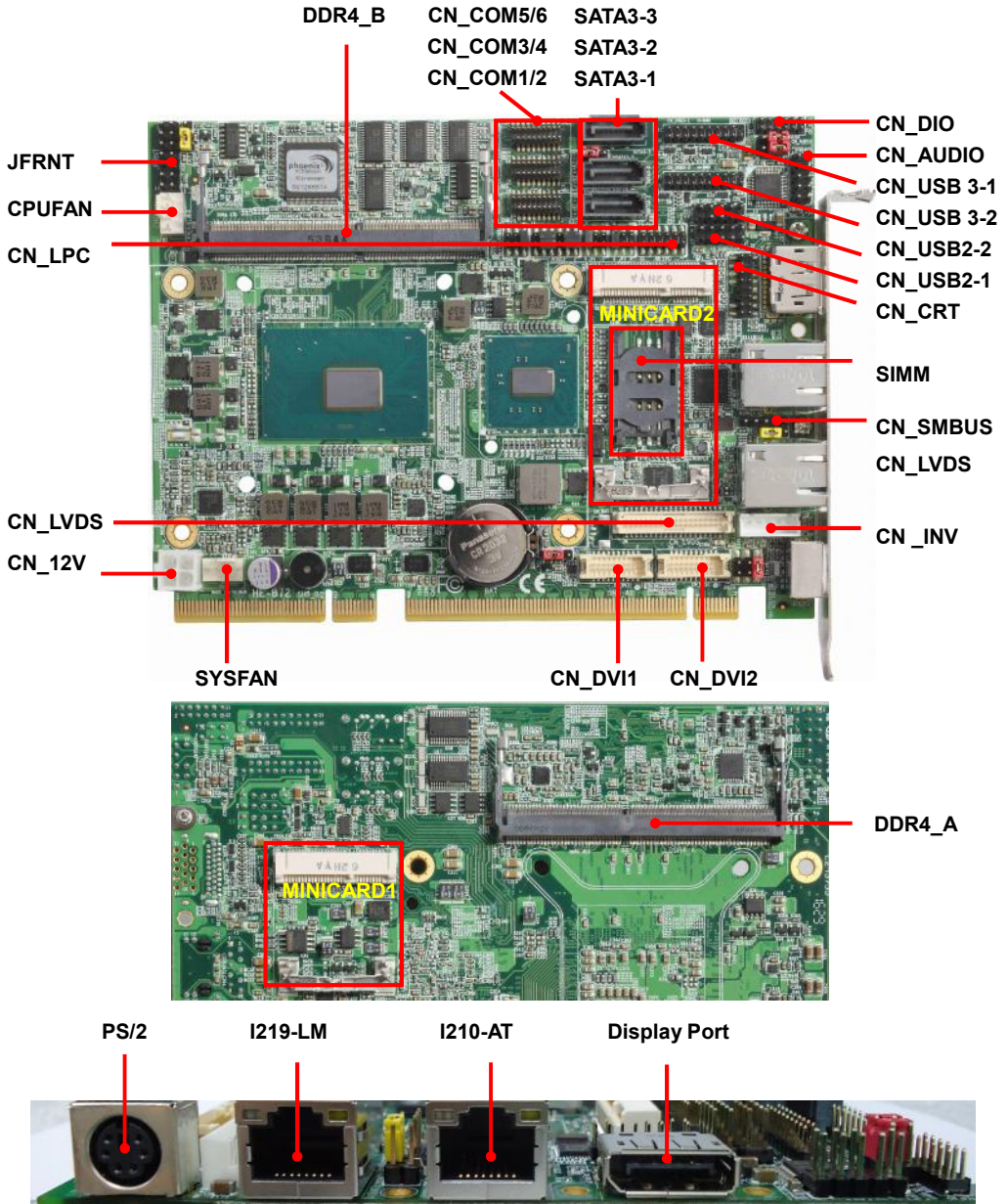
Power Requirement	Standard 24-pin ATX power from Backplane
Size & Thickness	168mm x 126mm (L x W)
Temperature	Operating within 0°C~60°C (32°F~140°F) Storage within -20°C~80°C (-4°F~176°F)
Relative Humidity	10%~90%, non-condensing

1.3 <Block Diagram>



Chapter 2 <Hardware setup>

2.1 <Connector Location and Reference>



2.1.1 <Internal connectors list>

Connector	Function
DDR4_A/B	260-pin DDR4 SO-DIMM slot
SATA3-1/2/3	7-pin Serial ATA3 connector
CN_AUDIO	5 x 2-pin audio pin header
CN_LPC	6 x 2-pin LPC pin header
CN_LVDS	20 x 2-pin LVDS connector
CN_INV	5-pin LCD inverter connector
CN_SMBUS	5-pin SMBus connector
CN_COM1/2	20-pin RS232/485/422 for COM2, and RS232 for COM1
CN_COM3/4 / 5/6	20-pin RS232 connector
CN_USB 2-1 / 2-2	5 x 2-pin USB2.0 pin header
CN_USB 3-1 / 3-2	10 x 2-pin USB3.0 pin header
CN_DIO	6 x 2-pin digital I/O connector
CN_CRT	16-pin VGA connector
CN_DVI1/2	10 x 2-pin DVI connector
CPUFAN	4-pin CPU fan connector
SYSFAN	4-pin system fan connector
JFRNT	14-pin front panel switch/indicator connector
MINI_CARD1	52-pin Half-Size MiniPCIe card slot
MINI_CARD2	52-pin MiniPCIe card slot
CN_12V	4-pin power input Terminal Block
SIMM	6-pin socket

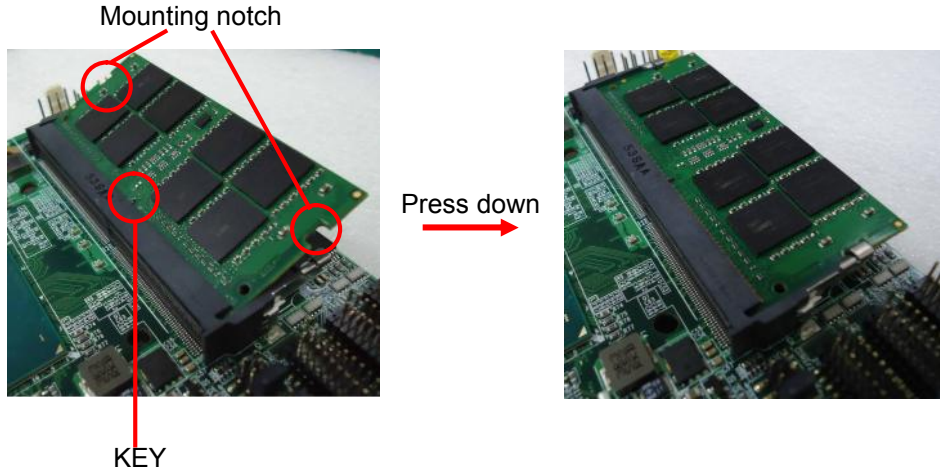
2.1.2 <External connectors list>

Connector	Function
DisplayPort	DisplayPort connector
RJ45-1/2	RJ45 connector
PS/2	PS/2 keyboard and mouse connector

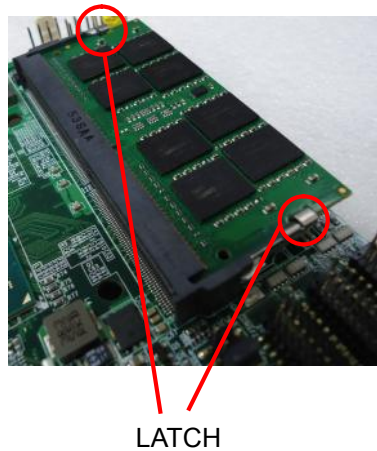
2.2 <Memory Setup>

In the process, the board must be powered off.

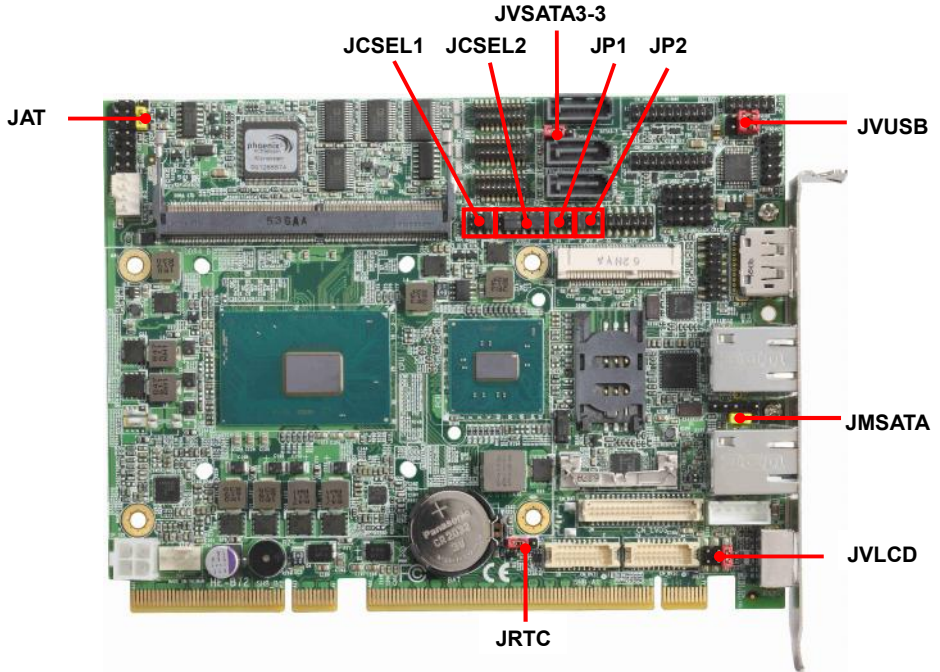
1. Put the memory tilt into the slot. Note the Memory notch key aligned slot key.
2. Then press down till lock into the mounting notch.



3. To remove the memory, push outward on both sides of the latch.



2.3 <Jumper Location and Reference>



2.3.1 <Jumper list>

Jumper	Function
JAT	Power mode select
JRTC	CMOS Normal/Clear Setting
JVLCD	Panel Voltage Setting
JMSATA	MiniCard 2 MSATA Setting
JP1	COM1 Voltage Setting (For Pin 9)
JP2	COM2 Voltage Setting (For Pin 9)
JVCSEL1	COM2 RS-232 RS422 RS485 Setting
JVCSEL2	COM2 RS-232 RS422 RS485 Setting
JVSATA3-3	Set 5V to SATA3-3 pin 7 (For SATADOM)
JVUSB	USB Voltage Setting (For CN_USB 3-1 / 3-2)

2.3.2 <Clear CMOS and Power on type selection>

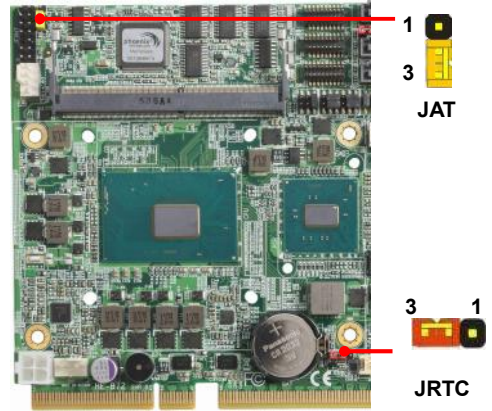
The board's data of CMOS can be setting in BIOS. If the board refuses to boot due to inappropriate CMOS settings, here is how to proceed to clear (reset) the CMOS to its default values.

JAT: AT/ATX mode select jumper

Jumper settings	Function
1-2	AT mode
2-3	ATX mode (Default)

JRTC: Clear CMOS data jumper

Jumper settings	Function
1-2	Clear CMOS
2-3	Normal (Default)

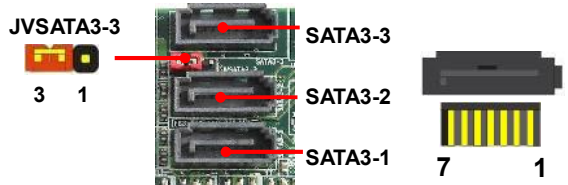


2.4 <I/O interface>

2.4.1 <Serial ATA interface>

SATA 1/2/3/4: SATA3 7-pin connector

Pin	Signal
1	GND
2	TX+
3	TX-
4	GND
5	RX-
6	RX+
7	GND

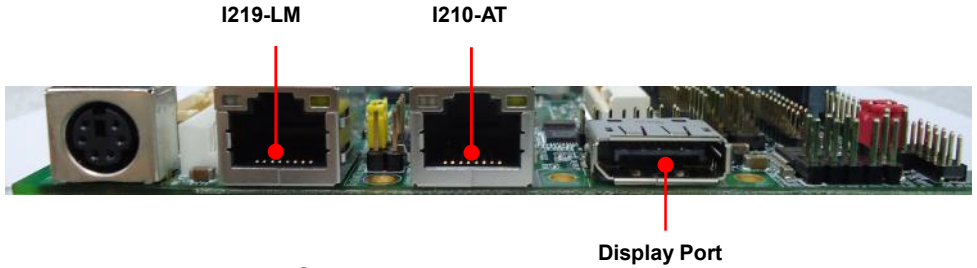


JVSATA3-3: SATA3/SATADOM mode select jumper (change pin7 to 5V)

Jumper settings	Function
1-2	SATA3-3 SATADOM
2-3	SATA3-3 SATA3 (Default)

2.4.2 <Ethernet interface>

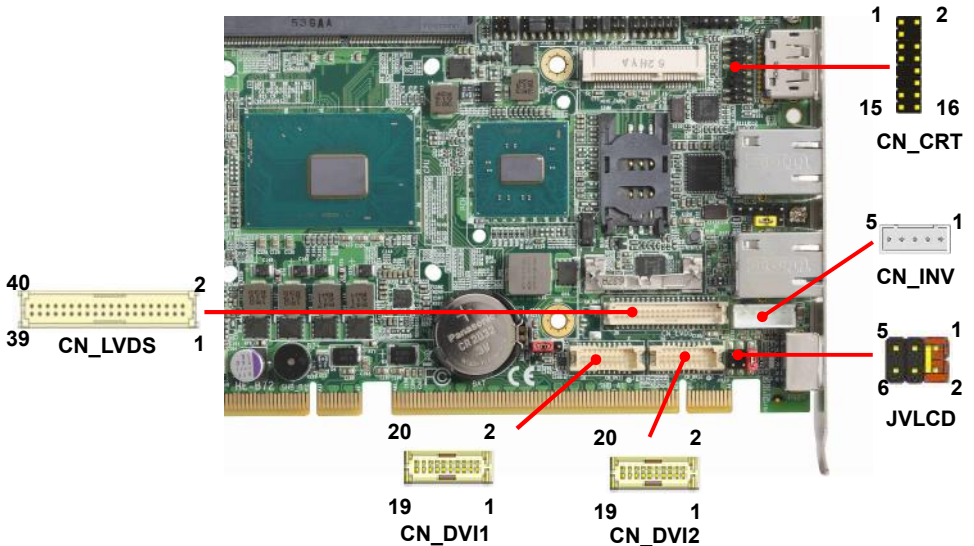
The board provide I219-LM PHY Gigabit Ethernet and I210-AT Gigabit Ethernet on rear I/O. Intel I219-LM and I210 supports operation at 10/100/1000 Mb/s data rates, with IEEE802.3 compliance and Wake-On-LAN supported.



2.4.3 <Display interface>

Based on the 6th Gen CPU with built-in HD Graphics 530, VGA and DVI up to **1920x1080@60Hz**, DisplayPort up to **4096x2304@60Hz**. About the internal Display, LVDS (PTN3460) up to **1920x1200@60Hz** support 18/24-bit color depth and single/dual channel. About select LCD Panel Type in BIOS, please refer **Appendix B**.

The built-in HD Graphics support triple display function with clone mode and extended mode.



CN_CRT: VGA 16-pin connector (Pitch 2.00 mm)

Pin	Signal	Pin	Signal
1	BR	2	BG
3	BB	4	NC
5	IOGND1	6	IOGND1
7	IOGND1	8	IOGND1
9	NC	10	IOGND1
11	NC	12	5VCCA
13	5HSYNC	14	5VSYNC
15	5VCLK	16	NC

CN_LVDS: LVDS 40-pin connector (Model: HIROSE DF13-40DP-1.25V compatible)

Pin	Signal	Pin	Signal
2	Set by JVLCD	1	Set by JVLCD
4	Detect (Active low)	3	GND
6	A_LVDS_0-	5	B_LVDS_0-
8	A_LVDS_0+	7	B_LVDS_0+
10	GND	9	GND
12	A_LVDS_1-	11	B_LVDS_1-
14	A_LVDS_1+	13	B_LVDS_1+
16	GND	15	GND
18	A_LVDS_2-	17	B_LVDS_2-
20	A_LVDS_2+	19	B_LVDS_2+
22	GND	21	GND
24	A_LVDS_CLK-	23	B_LVDS_3-
26	A_LVDS_CLK+	25	B_LVDS_3+
28	GND	27	GND
30	A_LVDS_3-	29	B_LVDS_CLK-
32	A_LVDS_3+	31	B_LVDS_CLK+
34	GND	33	GND
36	LVDS_DDCSCL	35	NC
38	LVDS_DDCSDA	37	NC
40	NC	39	NC

Note: Pin4 only need to be connected to GND

CN_INV: LVDS 5-pin Backlight power connector

Pin	Signal
1	12V
2	Backlight Control
3	GND
4	GND
5	Enable Backlight

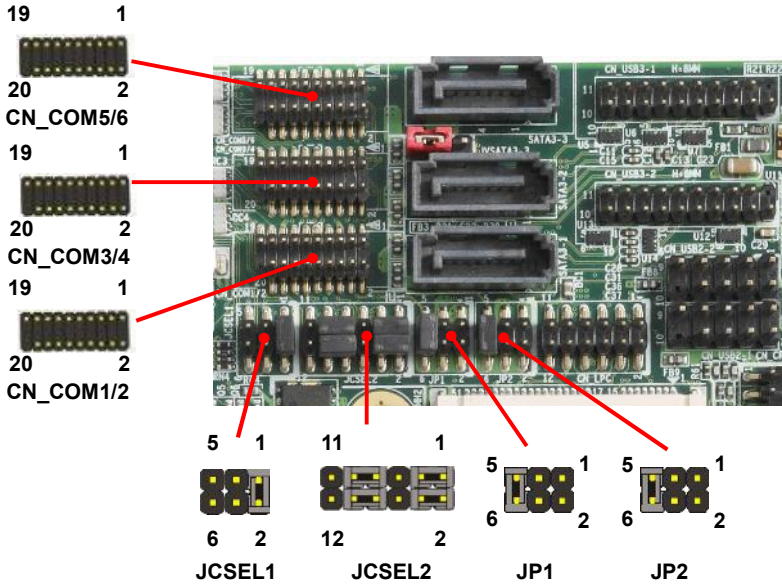
JVLCD: LVDS panel power select jumper

Jumper settings	Function
1-2	3.3V (Default)
3-4	5V
5-6	12V

CN_DVI: DVI onboard 20-pin connector

Pin	Signal	Pin	Signal
1	+5V	2	N/C
3	HPD	4	Ground
5	TMDSTX0N	6	TMDSTX0P
7	Ground	8	TMDSTX1N
9	TMDSTX1P	10	Ground
11	TMDSTX2N	12	TMDSTX2P
13	Ground	14	Ground
15	TMDSTXCP	16	Ground
17	DVI_DA	18	DVI_SL
19	N/C	20	N/C

2.4.4 <Serial Port interface>

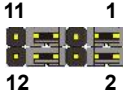


COM1/2:COM 20-pin header (Pitch 2.54 x 1.27mm)

Pin	Signal	Pin	Signal
1	DCD1	2	RXD1
3	TXD1	4	DTR1
5	GND1	6	DSR1
7	RTS1	8	CTS1
9	RI1	10	N/C
11	DCD2/422TX-/485-	12	RXD2/422TX+/485+
13	TXD2/422RX+	14	DTR2/422RX-
15	GND	16	DSR2
17	RTS2	18	CTS2
19	RI2	20	N/C

Use JCSEL1 and JCSEL2 to select communication mode

JCSEL1, JCSEL2: For configure COM2 communication mode

Function	JCSEL1	JCSEL2
RS232 (Default)		
RS485		
RS422		

COM3/4: COM 20-pin header (Pitch 2.54 x 1.27mm)

Pin	Signal	Pin	Signal
1	DCD1	2	RXD1
3	TXD1	4	DTR1
5	GND	6	DSR1
7	RTS1	8	CTS1
9	RI1	10	NC
11	DCD2	12	RXD2
13	TXD2	14	DTR2
15	GND	16	DSR2
17	RTS2	18	CTS2
19	RI2	20	Key

COM5/6: COM 20-pin header (Pitch 2.54 x 1.27mm)

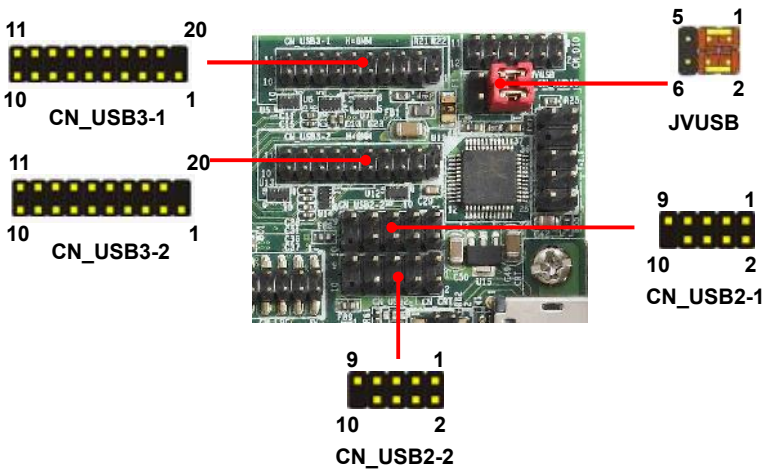
Pin	Signal	Pin	Signal
1	DCD1	2	RXD1
3	TXD1	4	DTR1
5	GND	6	DSR1
7	RTS1	8	CTS1
9	RI1	10	NC
11	DCD2	12	RXD2
13	TXD2	14	DTR2
15	GND	16	DSR2
17	RTS2	18	CTS2
19	RI2	20	Key

JP1, JP2: COM1, COM2 pin-9 setting

Jumper settings	Function
1-2	5V
3-4	12V
5-6	RI (Default)

Effective patterns of connection: 1-2 / 3-4 / 5-6

Other may cause damage

2.4.5 <USB interface>


CN_USB 2-1/2-2: USB2.0 10-pin header (Pitch 2.54 mm)

Pin	Signal	Pin	Signal
1	5VSB	2	5VSB
3	DATA0-	4	DATA1-
5	DATA0+	6	DATA1+
7	GND	8	GND
9	GND	10	Key

Install USB3.0 Driver If you want to use CN_USB 2-1/2-2 in Windows7.

CN_USB3-1/3-2: USB3.0 20-pin header (Pitch 2.00 mm)

Pin	Signal	Pin	Signal
1	VCC (5V_SB/ 5V)	20	NC
2	USB3.0_RX0-	19	VCC (5V_SB/ 5V)
3	USB3.0_RX0+	18	USB3.0_RX1-
4	Ground	17	USB3.0_RX1+
5	USB3.0_TX0-	16	Ground
6	USB3.0_TX0+	15	USB3.0_TX1-
7	Ground	14	USB3.0_TX1+
8	Data0-	13	Ground
9	Data0+	12	Data1-
10	NC	11	Data1+

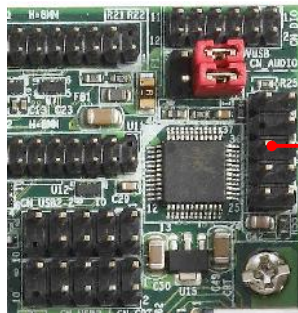
JVUSB: 6-pin Power select jumper (For CN_USB 3-1 / 3-2)

Pin	Description
1-3 & 2-4	5V_SB
3-5 & 4-6	5V

Default: 1-3 & 2-4

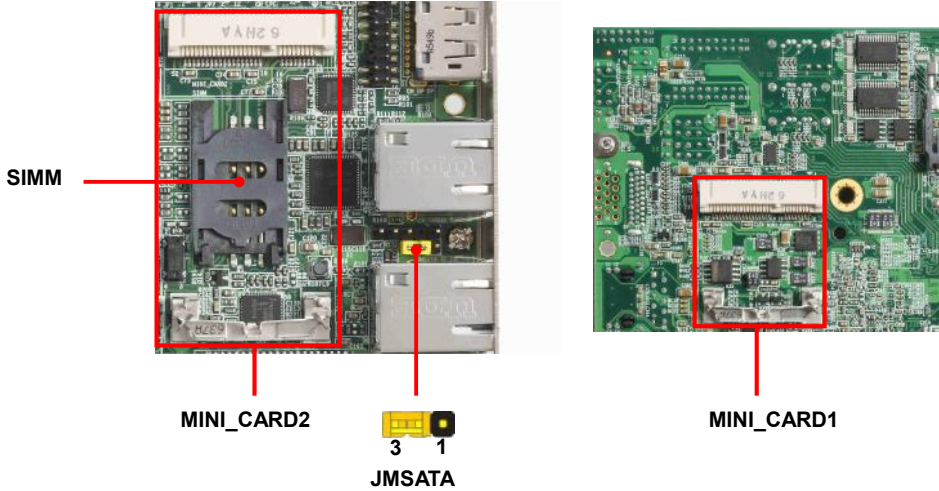
Effective patterns of connection: 1-3 & 2-4 or 3-5 & 4-6

2.4.6 <Audio interface>


CN_AUDIO: Front panel audio 10-pin header (Pitch 2.54mm)

Pin	Signal	Pin	Signal
1	MIC_L	2	GND
3	MIC_R	4	NC
5	FP_OUT_R	6	MIC_DETECT
7	SENSE	8	Key
9	FP_OUT_L	10	FP_OUT_DETECT

2.4.7 <Expansion slot>

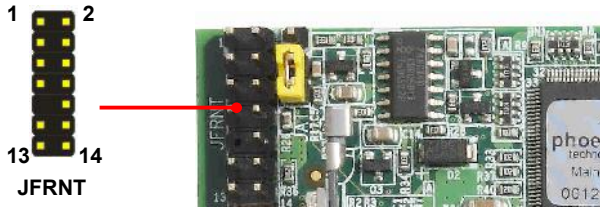


MINI_CARD2 support mSATA by JMSATA, and connect SIM card with 3G module.

JMSATA: Setting MINI_CARD2 to support PCIe/mSATA

Jumper settings	Function
1-2	Support mSATA
2-3	Normal operation (Default)

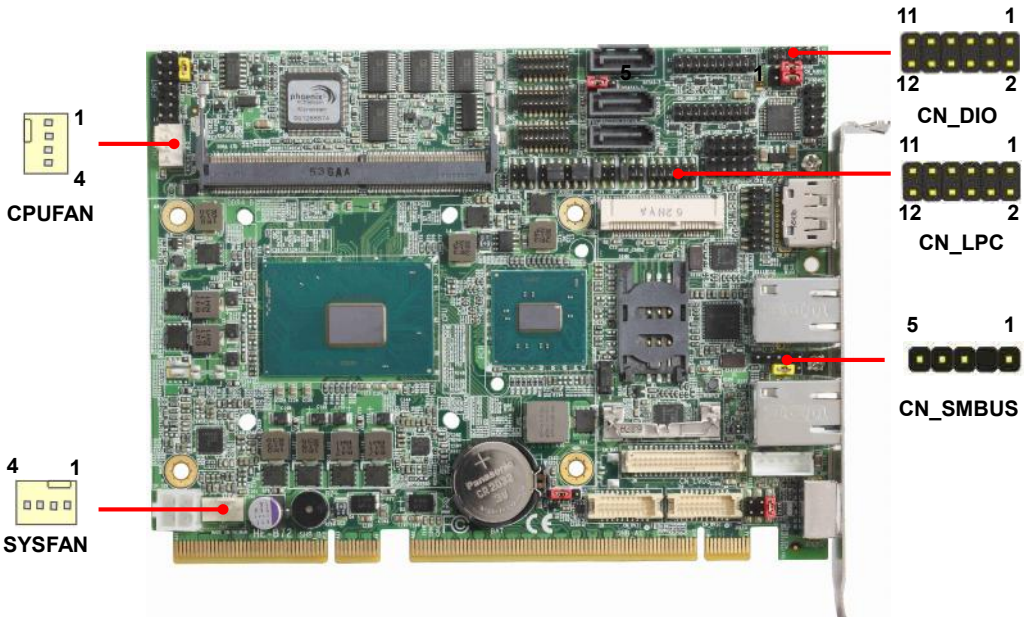
2.4.8 <Front panel switch and indicator>



JFRNT: Front panel switch and indicator 14-pin header (Pitch 2.54mm)

Pin	Signal	Pin	Signal
1	HDD_LED+	2	Power_LED+
3	HDD_LED-	4	NC
5	Reset+	6	Power_LED-
7	Reset-	8	Speaker+
9	Key	10	NC
11	Power_ON+	12	NC
13	Power_ON-	14	Speaker-

2.4.9 <GPIO and Other interface>



When using GPIO function, please note:

As Output: **Open-drain**, most applications **need use an external pull up resistor. (If not may cause damage)**

As Input: **TTL-level**.

GPIO DC characteristics

Parameter	SYM	MIN	TYP	MAX	UNIT	Conditions
Input Low Voltage	V_{IL}			0.8	V	
Input High Voltage	V_{IH}	2.0			V	
Output Low Voltage	V_{OL}			0.4	V	$I_{OL} = 12mA$
Input High Leakage	I_{LH}			+10	μA	$V_{IN} = 3.3V$
Input Low Leakage	I_{LL}			-10	μA	$V_{IN} = 0V$

CN_DIO: GPIO 12-pin header (Pitch 2.00mm)

Pin	Signal	Pin	Signal
1	GND	2	GND
3	GPIO0	4	GPIO4
5	GPIO1	6	GPIO5
7	GPIO2	8	GPIO6
9	GPIO3	10	GPIO7
11	5V	12	12V

CN_LPC: LPC 12-pin header (Pitch 2.00mm)

Pin	Signal	Pin	Signal
1	CLK	2	RST
3	-LFRAME	4	LAD3
5	LAD2	6	LAD1
7	LAD0	8	3.3V
9	SERIRQ	10	GND
11	3.3VSB	12	NC

CN_SMBUS: SMBus 5-pin connector (Pitch 2.54mm)

Pin	Signal
1	5V
2	NC
3	SMBDAT
4	SMBCLK
5	GND

CPUFAN: CPU cooler fan 4-pin connector

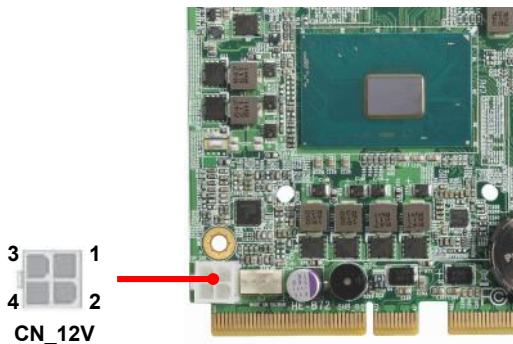
Pin	1	2	3	4
Signal	GND	12V	Sensor	Control

SYSFAN: System cooler fan 4-pin connector

Pin	1	2	3	4
Signal	GND	12V	Sensor	Control

2.5 <Power supply>

2.5.1 <Power input> (need Backplane)



CN_12V: ATX12V 4-pin connector

Pin	Signal	Pin	Signal
1	GND	2	GND
3	12V	4	12V

Appendix A <Flash BIOS>

A.1 <Flash tool>

The board is based on Phoenix BIOS and can be updated easily by the BIOS auto flash tool. You can download the tool online at the address below:

[FPT TOOL](#)

The tool's file name is "fpt.exe", it's the utility that can write the data into the BIOS flash chip and update the BIOS.

A.2 <Flash BIOS process>

1. Please make a bootable UFD which can boot into DOS environment.
2. Unzip the flash tool and copy it into bootable UFD.
3. Add a bin file to the same folder..
4. Power on the system and flash the BIOS under the DOS environment.

(Command: fpt -savemac -f xxx.bin)

5. Power off the system and then power on.

Appendix B <LCD Panel Type select>

According your panel, it need to select the correct resolution in the BIOS. If there is no fit your panel type, please feedback for us to make OEM modol.

You can find the setting from

[Advanced] → [Intel Advanced Menu]

→ [SA configuration] → [Graphics confuguration] → [LCD control]

→ [LCD Panel Type]

Phoenix SecureCore Technology Setup	
Advanced	
Intel Advanced Menu	Item Specific Help
PCI Subsystem Settings ACPI Settings CPU Configuration Power & Performance System Agent (SA) Configuration PCIO Configuration Manageability Application Configuration Super IO Chip	System Agent (SA) Parameters
F1 Help Esc Exit <> Select Menu +/~ Change Values Enter Select Sub-Menu	F9 Setup Defaults F10 Save and Exit

Phoenix SecureCore Technology Setup	
Advanced	
Graphics Configuration	Item Specific Help
Internal Graphics [Auto] GTT Size [8MB] Aperture Size [256MB] DUMT Pre-Allocated [32M] DUMT Total Gfx Mem [256M] Gfx Low Power Mode [Enabled] UDD Enable [Enabled] HDCP Support [Enabled] Algorithm [One-time] PM Support [Enabled] PAUP Enable [Enabled] Cd Clock Frequency [675 Mhz] IUER Button Enable [Disabled] LCD Control Intel(R) Ultrabook Event Support	LCD Control
F1 Help Esc Exit <> Select Menu +/~ Change Values Enter Select Sub-Menu	F9 Setup Defaults F10 Save and Exit

BIOS panel type selection form (BIOS Version:1.0)

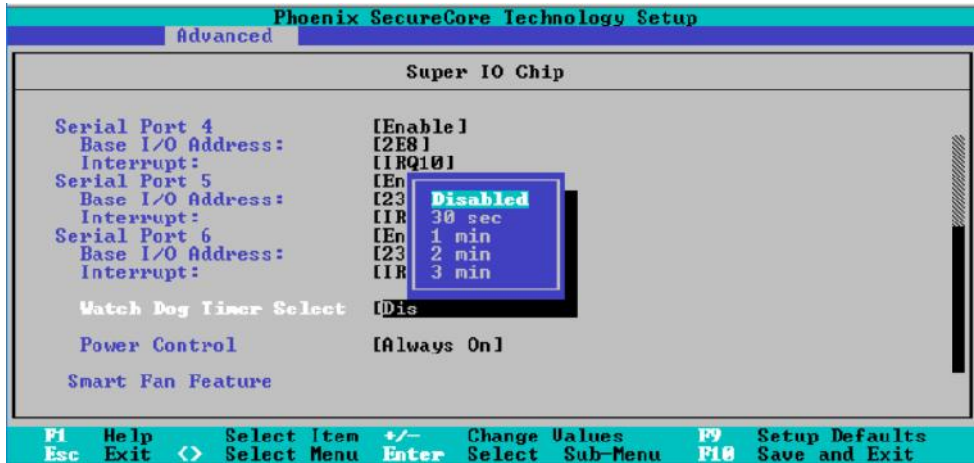
Single / Dual channel		Single / Dual channel	
NO.	Type	NO.	Type
1	VBIOS DEFAULT	9	1366 x 768
2	640 x 480	10	1680 x 1050
3	800 x 600	11	1920 x 1200
4	1024 x 768	12	1400 x 900
5	1280 x 1024	13	1600 x 900
6	1400 x 1050 Reduced Blanking	14	1024 x 768
7	1400 x 1050 non-Reduced Blanking	15	1280 x 800
8	1600 x 1200	16	1920 x 1080
		17	OEM

Appendix C <Programmable Watch Dog Timer>

The watchdog timer makes the system auto-reset while it stops to work for a period. The integrated watchdog timer can be setup as system reset mode by program. You can select Timer setting in the BIOS, after setting the time options, the system will reset according to the period of your selection.

Find the setting from

[Advanced] → [Intel Advanced Menu] → [Super IO Chip]



Timeout value range

1 to 255 Minute and Second

Program sample

Watchdog timer setup as system reset with 5 second of timeout

- o 4E 87 ;enter configuration
- o 4E 87
- o 4E 07
- o 4F 08 ;select Logical Device
- o 4E 30

- o 4F 01 ; activate WDTO# function
- o 4E F5
- o 4F 00 ;set "00" is second mode, set "04" is minute mode
- o 4E F6
- o 4F 05 ;00h: Timeout Disable
- ;01h: Timeout occurs after 1 minute only
- ;02h: Timeout occurs after 2 second/minute
- ;03h: Timeout occurs after 3 second/minute
- ⋮
- ;FFh: Timeout occurs after 255 second/minute
- (The deviation is approx 1 second.)

For further information, please refer to Nuvoton NCT6106D datasheet

Appendix D <Hardware monitor >

Find the setting from [Misc] → [SIO NCT6106D Hardware Monitor]

Phoenix SecureCore Technology Setup	
Misc	
Hardware Monitor	Item Specific Help
System Temperature	[30.5 C]
CPU Temperature	[29.5 C]
System Fan Speed	[0 RPM]
CPU Fan Speed	[6585 RPM]
AUX Fan Speed	[0 RPM]
Battery 3V (VBAT)	[3.000 V]
CPU VCORE	[1.000 V]
12V	[11.985 V]
5V	[5.000 V]

F1	Help	Select Item	+/-	Change Values	F9	Setup Defaults
Esc	Exit	<>	Enter	Select Sub-Menu	F10	Save and Exit

Appendix E <Programmable GPIO >

The GPIO' can be programmed with the MS-DOS debug program using simple IN/OUT commands.

The DC characteristics please refer to GPIO paragraph

GPIO	0	1	2	3	4	5	6	7
bit	0	1	2	3	4	5	6	7

- o 4E 87 ;enter configuration
- o 4E 87
- o 4E 07
- o 4F 07 ;select Logical Device
- o 4E 30
- o 4F 10 ;activate GPIO function (The board use GPIO3)
- o 4E F0
- o 4F XX ;set "01" GPIO as input, set "00" GPIO as output
- o 4E F1
- o 4F XX ;if set GPIO as output, this register's value can be set "00~ FF"

Optional

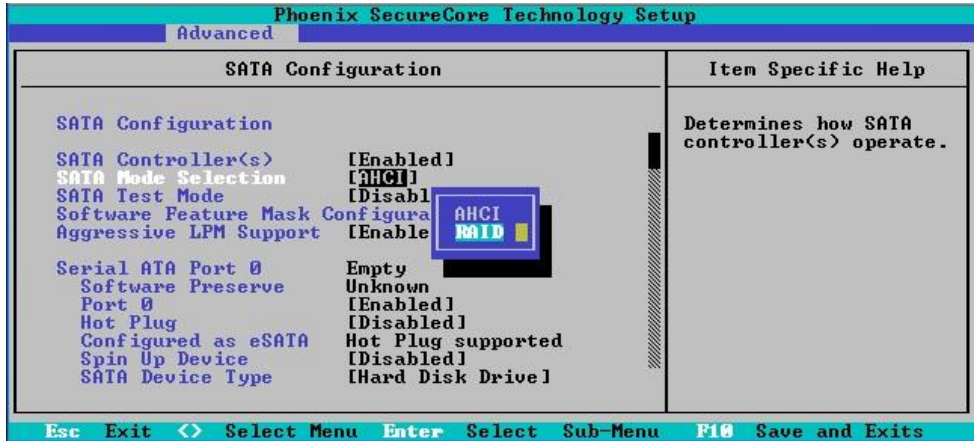
- o 4E F2
- o 4F XX ;set "01", the respective bit are inverted (Both input and output)
- ;set "00", the respective bit are normal

For further information, please refer to Nuvoton NCT6106D datasheet

Appendix F <RAID Setting>

When use RAID function, it need to enter the BIOS set RAID mode first.

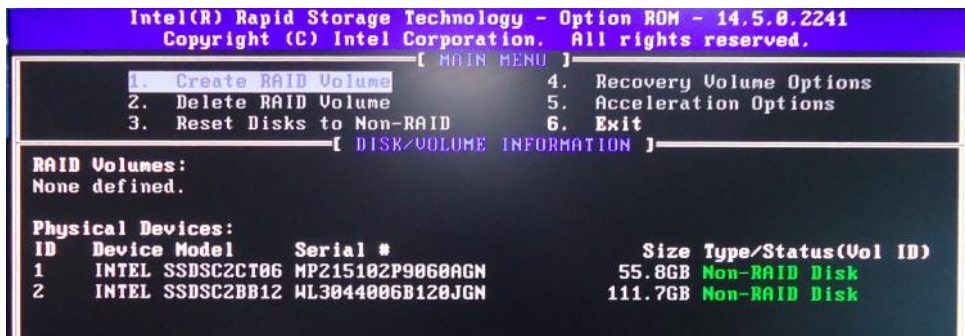
[Advanced] → [Intel Advanced Menu] → [PCH-IO Configuration]
 → [SATA Configuration] → [SATA Mode Selection]



If this screen stop time is too short, it can be set in the BIOS.

[Advanced] → [Intel Advanced Menu] → [PCH-IO Configuration]
 → [SATA Configuration] → [Software Feature Mask Configuration]
 → [OROM UI Normal Delay] → [8 sec] **(Need to set RAID mode first)**

At boot time, press <CTRL + I> to enter the RAID configuration menu.



Contact information

Any advice or comment about our products and service, or anything we can help you please don't hesitate to contact with us. We will do our best to support you for your products, projects and business.

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